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A hands-on introduction to FPGA prototyping and SoC design This is the successor edition of the popular FPGA Prototyping by Verilog Examples text. It follows the same "learning-by-doing" approach to teach the fundamentals and practices of HDL synthesis and FPGA prototyping. The new edition uses a coherent series of examples to demonstrate the process to develop sophisticated digital circuits and IP (intellectual property) cores, integrate them into an SoC (system on a chip) framework, realize the system on an FPGA prototyping board, and verify the hardware and software operation. The examples start with simple gate-level circuits, progress gradually through the RT (register transfer) level modules, and lead to a functional embedded system with custom I/O peripherals and hardware accelerators. Although it is an introductory text, the examples are developed in a rigorous manner, and the derivations follow the strict design guidelines and coding practices used for large, complex digital systems. The book is completely updated and uses the SystemVerilog language, which "absorbs" the Verilog language. It presents the hardware design in the SoC context and introduces the hardware-software co-design concept. Instead of treating examples as isolated entities, the book integrates them into a single coherent SoC platform that allows readers to explore both hardware and software "programmability" and develop complex and interesting embedded system projects. The new edition: Adds four general-purpose IP cores, which are multi-channel PWM (pulse width modulation) controller, I2C controller, SPI controller, and XADC (Xilinx analog-to-digital converter) controller. Introduces a music synthesizer constructed with a DDFS (direct digital frequency synthesis) module and an ADSR (attack-decay-sustain-release) envelope generator. Expands the original video controller into a complete stream based video subsystem that incorporates a video synchronization circuit, a test-pattern generator, an OSD (on-screen display) controller, a sprite generator, and a frame buffer. Provides a detailed discussion on blocking and nonblocking statements and coding styles. Describes basic concepts of software-hardware co-design with Xilinx MicroBlaze MCS soft-core processor. Provides an overview of bus interconnect and interface circuit. Presents basic embedded system software development. Suggests additional modules and peripherals for interesting and challenging projects. FPGA Prototyping by SystemVerilog Examples makes a natural companion text for introductory and advanced digital design courses and embedded system courses. It also serves as an ideal self-teaching guide for practicing engineers who wish to learn more about this emerging area of interest. Scientists can use molecular profiling microarrays to compare healthy cells with their diseased counterparts and develop gene-specific treatments. Finding the best way to interpret original profiling data into accurate trends, however, continues to drive the development of normalization algorithms and software tools. Methods in Microarray Normalization compiles the most useful and novel techniques for the first time into a single, organized source. Experts in the field provide a diverse view of the mathematical processes that are important in normalizing data and avoiding inherent systematic biases. They also review useful software, including discussions on key algorithms, comparative data, and download locations. The book discusses the use of early normalization techniques for new profiling methods and includes strategies for assessing the utility of various normalization algorithms. It presents the latest microarray innovations from companies such as Agilent, Affymetrix, and GeneGo as well as new normalization methods for protein and CGH arrays, many of which are applicable for antibody, microRNA, methylation, and siRNA arrays. Methods in Microarray Normalization provides scientists with a complete resource on the most effective tools available for maximizing microarray data in biochemical research. Daniel E. Levy, editor of the Drug Discovery Series, is the founder of DEL BioPharma, a consulting service for drug discovery programs. He also maintains a blog that explores organic chemistry. The official "Fedora 14 User Guide" is focused on the end-user looking to accomplish standard desktop computer user tasks, such as browsing the web, reading and sending email, and doing office productivity work. Recent radical changes in timecode technology, location shooting and post-production working practices have been brought about by the fragmentation of the television programme making industry and by a dramatic increase in affordable digital transmission and editing equipment and systems. With the expansion of non-traditional television service producers (cable, satellite and video-on-demand) almost anything hoes as far as shooting and editing formats are concerned. Timecode: A User's Guide is an indispensable reference for anyone needing to get to grips with the many aspects of timecode, whether in-house or on location. Taking into account these changes this book has now been brought completely up to date to include: * timecode and DVD, LTC & VITC in HANC packets in the serial digital TV interfaces * timecode in IEEE1395 (Firewire) * timecode and digital video cassettes * new recording formats of DVD, DV mini cassettes and D6 are included * 4:3 scanning for wide-screen films - standards updated * new material to cover new working practices * new appendices to cover the global LF time data transmissions and time data embedded in BBC transmissions Advice is also given on avoiding and remedying faults and errors. The essential guide to getting started with the Raspberry Pi @ The Raspberry Pi has been a success beyond the dream of its creators. Their goal, to encourage a new generation of computer programmers who understand how computers work, is well under way. Raspberry Pi User Guide 2e is the newest edition of the runaway bestseller written by the Pi's co-creator, Eben Upton, and tech writer Gareth Halfacree. It contains everything you need to know to get the Pi up and running, including how to: Connect a keyboard, mouse, monitor and other peripherals Install software and configure your Raspberry Pi Master basic Linux system administration Set up your Raspberry Pi as a productivity machine, multimedia centre, or web server Write programmes in Scratch and Python Use the GPIO port and add-on boards to connect your Raspberry Pi for use in electronics projects Updated to cover the release of the Camera Board, the introduction of the Pi Store, NOOBS and much more, Raspberry Pi User Guide 2nd edition is the perfect companion for getting the most out of the computing phenomenon, the Raspberry Pi. Eben Upton is the co-creator of the Raspberry Pi board and the founder of the Raspberry Pi Foundation. Gareth Halfacree is a freelance technology journalist, open source advocate and erstwhile sysadmin. The official "Fedora 13 User Guide" is focused on the end-user looking to accomplish standard desktop computer user tasks, such as browsing the web, reading and sending email, and doing office productivity work. Evaluate MicroStrategy as a departmental solution. This book provides detailed information to download, install, configure, and use the MicroStrategy Reporting Suite. A hands-on introduction to FPGA prototyping and SoC design This Second Edition of the popular book follows the same "learning-by-doing" approach to teach the fundamentals and practices of VHDL synthesis and FPGA prototyping. It uses a coherent series of examples to demonstrate the process to develop sophisticated digital circuits and IP (intellectual property) cores, integrate them into an SoC (system on a chip) framework, realize the system on an FPGA prototyping board, and verify the hardware and software operation. The examples start with simple gate-level circuits, progress gradually through the RT (register transfer) level modules, and lead to a functional embedded system with custom I/O peripherals and hardware accelerators. Although it is an

introductory text, the examples are developed in a rigorous manner, and the derivations follow strict design guidelines and coding practices used for large, complex digital systems. The new edition is completely updated. It presents the hardware design in the SoC context and introduces the hardware-software co-design concept. Instead of treating examples as isolated entities, the book integrates them into a single coherent SoC platform that allows readers to explore both hardware and software "programmability" and develop complex and interesting embedded system projects. The revised edition: Adds four general-purpose IP cores, which are multi-channel PWM (pulse width modulation) controller, I2C controller, SPI controller, and XADC (Xilinx analog-to-digital converter) controller. Introduces a music synthesizer constructed with a DDFS (direct digital frequency synthesis) module and an ADSR (attack-decay-sustain-release) envelop generator. Expands the original video controller into a complete stream-based video subsystem that incorporates a video synchronization circuit, a test pattern generator, an OSD (on-screen display) controller, a sprite generator, and a frame buffer. Introduces basic concepts of software-hardware co-design with Xilinx MicroBlaze MCS soft-core processor. Provides an overview of bus interconnect and interface circuit. Introduces basic embedded system software development. Suggests additional modules and peripherals for interesting and challenging projects. The FPGA Prototyping by VHDL Examples, Second Edition makes a natural companion text for introductory and advanced digital design courses and embedded system course. It also serves as an ideal self-teaching guide for practicing engineers who wish to learn more about this emerging area of interest. The Fedora User Guide is focused on the end-user looking to accomplish standard desktop computer user tasks, such as browsing the web, reading and sending email, and doing office productivity work. The official "Fedora 12 User Guide" is focused on the end-user looking to accomplish standard desktop computer user tasks, such as browsing the web, reading and sending email, and doing office productivity work. The EXAMVIEW(R) Assessment Suite allows you to quickly and easily create, administer, and score paper and online tests. The EXAMVIEW(R) Assessment Suite includes the EXAMVIEW(R) Test Generator, EXAMVIEW(R) Test Manager, and EXAMVIEW(R) Test Player. Please refer to the EXAMVIEW(R) Assessment Suite User's Guide (Manual.pdf) on this CD for features and functions of the EXAMVIEW(R) programs. The first User's Guide to the National Electrical Code(R) explains basic principles of the NEC(R)! NFPA's 2002 Edition details and explains the basic NEC principles you must know to work effectively with the world's most widely used building code! Written by H. Brooke Stauffer, Director of Codes & Standards at the National Electrical Contractor's Association, User's Guide to the National Electric Code is the ideal starting point for electrical apprentices, and a useful reference for experienced pros. Launch your career in the electrical field-or get the NEC background you've been missing! Learn how to find your way around the 2002 NEC through text explaining: What's covered in each chapter of the NEC. Use it alongside your 2002 Code!How the National Electrical Code works with other NFPA electrical standards and building codes The NEC consensus development process and the significance of TIAs and Formal Interpretations The User's Guide offers expert analyses of technical requirements-the kind of information it can take years to acquire: The difference between GFPE and GFCI equipment Why terminals for ungrounded hot conductors must be color-distinguishable from the silver or white usedfor grounded conductors Reasons to use a multiwire branch circuit. The NEC tells you how to install it-only the User's Guide tells you why. Find examples of TVSS (transient voltage surge suppressors) and hundreds of other explanations. The MicroStrategy Office User Guide covers the instructions for using MicroStrategy Office to work with MicroStrategy reports and documents in Microsoft? Excel, PowerPoint, Word, and Outlook, to analyze, format, and distribute business data. This book helps readers to implement their designs on Xilinx® FPGAs. The authors demonstrate how to get the greatest impact from using the Vivado® Design Suite, which delivers a SoC-strength, IP-centric and system-centric, next generation development environment that has been built from the ground up to address the productivity bottlenecks in system-level integration and implementation. This book is a hands-on guide for both users who are new to FPGA designs, as well as those currently using the legacy Xilinx tool set (ISE) but are now moving to Vivado. Throughout the presentation, the authors focus on key concepts, major mechanisms for design entry, and methods to realize the most efficient implementation of the target design, with the least number of iterations. Every company that wants to continue growth needs their sales team to be proficient in finding and closing net-new opportunities. But, unfortunately, most sales persons are not good at gaining new business, much less performing even the most basic prospecting practices. There are a multitude of reasons for this phenomenon. But the biggest reason is that sales professionals are untrained in vital cold-calling techniques. Especially in the realm of cold-calling and prospecting. Further, there is a culture of sales resistance that exists, and few sales professionals are equipped to penetrate it. The Must-React System is written help all sales professionals master the art of persuasion, and especially in the important area of cold-calling, prospecting, sales pipeline development.

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